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L1	993	708/400-409.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 11:02
L2	727	Fourier and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 11:02
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L4	237	3 and revers\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 11:04
L5	6	4 and strid\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 11:05
L6	273	708/404.ccls. and @ad<"20000801"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 11:05
L7	98	6 and revers\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 11:06
L8	52	7 and butterfl\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 11:06

L9	2	8 and strid\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/26 11:06
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Practical Applications of DSP Devices, IEE Colloquium on
11 Jun 1990 Page(s):11/1 - 11/3[AbstractPlus](#) | [Full Text: PDF\(92 KB\)](#) IEE CNF

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 2. **Matrix representations for sorting and the fast Fourier transform**Sloate, H.;
Circuits and Systems, IEEE Transactions on
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